

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : David A. Zimlich Attorney Docket No.: 501265.01
Filed : December 29, 2003
Title : ACCESS CIRCUIT AND METHOD FOR ALLOWING EXTERNAL TEST
VOLTAGE TO BE APPLIED TO ISOLATED WELLS

INFORMATION DISCLOSURE STATEMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the reference set forth on the attached form PTO-1449 (a copy of the cited reference, as required under 37 C.F.R. § 1.98, is enclosed). Although the aforesaid reference is made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,
DORSEY & WHITNEY LLP



Steven H. Arterberry
Registration No. 46,314

EWB:dms

Enclosures:

Form PTO-1449
Cited References (1)

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FORM PTO-1449 (REV.7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 501265.01	APPLICATION NO. Not yet assigned
INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>		APPLICANT(S) David A. Zimlich	
		FILING DATE Concurrently herewith	GROUP ART UNIT Not yet assigned

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO
	AK					
	AL					
	AM					
	AN					
	AO					

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

	AP	Favrat, Pierre et al., "A High-Efficiency CMOS Voltage Doubler", IEEE Journal of Solid-State Circuits, Vol. 33, No. 3, March 1998, pp. 410-416.
	AQ	

EXAMINER**DATE CONSIDERED**

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).